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U.S. **UTILITY** Patent Application

O.I.P.E. SCANNED SS

PATENT DATE

APPLICATION NO. CONT/PRIOR 09/751979

CLASS

SUBCLASS ART UNIT

EXAMINER Goudrean

APPLICANTS Absushi Mishizawa

Menufacturing method semiconductor integrated circuit including simultaneous formation of via hole reaching metal miring and concave! grouve in interleyter film and semiconductor integrated circuit manufactured with the manufacturing method

PTO-2040 1289

ISSUING CLASSIFICATION												
ORIGINAL		CROSS REFERENCE(S)										
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)									
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INTERNATIONAL CLASSIFICATION		430	.5					, , ,				
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TERMINAL		DRAWINGS		CLAIMS ALLOWED			
☐ DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.		
The term of this patent				NOTICE OF ALLOWANCE MAILED			
subsequent to (date) has been disclaimed.		Examiner)	(Date)	8-12	-03		
The term of this patent shall not extend beyond the expiration date of U.S Patent. No	Office	Dream	-8-11-03'	ISSUE FEE			
·	PRIMA	Examiner	*	Amount Due	Date Paid		
The terminalmonths of this patent have been disclaimed.	MW (Legal Instrum	OUS W	8/B/(Date)	ISSUE BAT	TCH NUMBER		
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